

The TriPeak



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MontBlanc = A ManyCore (NVidia) + a MultiCore (ARM)

Maxeler = A FineGrain DataFlow (FPGA)

How about a happy marriage
of MontBlanc and Maxeler?

In each happy marriage,
it is known who does what :)

Core of the Symbiotic Success:

An intelligent scheduler,
partially implemented for compile time,
and partially for run time.

At compile time:

Checking what part of code fits where
(MontBlanc or Maxeler).

At run time:

Rechecking the compile time decision,
based on the current data values

Essence:

The three concepts are defined precisely,
and tested against static data,
and again against dynamic data.









Q&A

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