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Dr Veljko Milutinovic
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POSITIONS

University of Belgrade

Professor, since 1995

Michael Pupin Institute

Research Associate, 1975-1983

University of Novi Pazar

Professor, 2016/2017

University of Kragujevac

Professor, since 2016

Purdue University

Tenure Track Assistant Professor and Visiting Professor, 1983-1995;
Visiting Lecturer, 2015-2027

Indiana University

Adjunct Professor, 2015-2026

MIT

Visiting Lecturer, 2015-2019

Harvard University

Visiting Lecturer, 2015-2019

Technical University of Vienna

Adjunct Professor, 2018

University of Vienna

Visiting Professor, 2018

Technical University of Graz

Adjunct Professor, since 2018

Alma Mater University of Vienna, by the European Academy of Sciences and Arts

Visiting Professor, 2026-2031

Universitat Politècnica de Catalunya

Visiting Lecturer, 1995

Universitat Politècnica de Valencia

Visiting Lecturer, 1995

Universitat Politècnica de Madrid

Visiting Lecturer, 1995

Gijón - Universidad de Oviedo

Visiting Lecturer, 1995

Scuola Superiore Sant'Anna
Visiting Lecturer, 1990

Università di Pisa
Visiting Lecturer, 1990

Università degli Studi di Siena
Visiting Lecturer, 1995; 2005; 2015; 2025

Università di Salerno
Visiting Lecturer, 2025; 2026

University of Sydney
Visiting Lecturer, 1988

University of Technology Sydney
Visiting Lecturer, 1988

University of Tasmania
Visiting Lecturer, 1988

University of Brisbane
Visiting Lecturer, 1991

University of Ljubljana
Visiting Lecturer, 2005-2020

University of Zagreb
Visiting Lecturer, 2005

University of Montenegro
Visiting Lecturer, 1990-1995; 2015-2025

University of Skopje
Visiting Lecturer, 2015

Boğaziçi University
Visiting Lecturer, 2020

Hebrew University of Jerusalem
Visiting Lecturer, 2025

University of Sharjah, UAE
Visiting Lecturer, 2025

Shandong University
Visiting Lecturer, 2020

EDUCATION

Ph.D., University of Belgrade (Serbia), 1982

B.Sc., University of Belgrade (Serbia), 1975

MAJOR PROJECTS

GaAs Microprocessor for DARPA

Data Flow Supercomputer for Maxeler

Heritage Mining with Artificial Intelligence

New Holistic Methodologies for Project Management

HONORS

Life Fellow of the IEEE

Member of the Academy of Europe and a Former Honorary Treasurer

Member of the Serbian Academy of Engineering

Foreign Member of the Montenegrin Academy of Sciences and Arts

PUBLICATIONS

Journal Articles (Q1 top 10% at WoS)

1. F. Celli, A. Kartelj, M. Dordevič, D. Suhartono, V. Filipovič, V. Milutinović, ... & B. Lepri, Twenty years of personality computing: Threats, challenges and future directions, *ACM Computing Surveys*, 2026. (M21a+)
2. V. Milutinovic, D. Milutinovic, B. Furht, On mapping of topological structures and mathematical algorithms onto specialized architectures for graph-based computing, *Journal of Big Data*, 2026. (M21a+)
3. N. Korolija, V. Milutinović, B. Furht, Algorithm profiling for architectures with dataflow accelerators, *Journal of Big Data*, 12(1), 183., 2025. (M21a+)
4. Z. Babovic, ..., V. Milutinovic et al., "Research in Computing-Intensive Simulations for Nature-Oriented Civil-Engineering and Related Scientific Fields, Using Machine Learning and Big Data: An Overview of Open Problems," *Springer Journal of Big Data*, May 2023. (M21a+)
5. Z. Babovic, ..., V. Milutinovic et al., "Teaching Computing for Complex Problems in Civil Engineering and Geosciences Using Big Data and Machine Learning: Synergizing Four Different Computing Paradigms and Four Different Management Domains," *Springer Journal of Big Data*, June 2023. (M21a+)
6. A. Kos, V. Milutinovic, A. Umek, "Challenges in wireless communication for connected sensors and wearable devices used in sport biofeedback applications," *Future Generation Computer Systems*, vol. 92, pp. 582-592, 2019, IF: 6,125, (M21a).
7. R. Trobec, R. Vasiljević, M. Tomašević, V. Milutinović, R. Beivide, and M. Valero, "Interconnection Networks in Petascale Computer Systems: A Survey," *ACM Computing Surveys*, Vol.49 (3), (September 2016), 24 pages, 2016, IF: 6,748 (M21a).
8. M. Flynn, O. Mencer, V. Milutinovic, G. Rakocevic, P. Stenström, R. Trobec, M. Valero, "Moving from Petaflops to Petadata," *Communications of the ACM*, Vol. 56 (5), pp. 39-42, 2013, IF:2,863 (M21a).
9. I. Ekmecic, I. Tartalja, V. Milutinovic, "A survey of heterogeneous computing: concepts and systems," *Proceedings of the IEEE*, Vol. 84 (8), pp. 1127-1144, 1996, IF: 2,699/1997 (M21a).
10. B. Perunicic, S. Lakhani, V. Milutinovic, "Stochastic Modeling and Analysis of Propagation Delays in GaAs Adders," *IEEE Transactions on Computers*, Vol. 40(1), pp. 31-45, 1991, IF: 1,208/1992 (M21a).
11. V. Milutinovic, D. Fura, W. Helbig, "Pipeline Design Tradeoffs in a 32-bit Gallium Arsenide Microprocessor," *IEEE Transactions on Computers*, Vol. 40(11), pp. 1214-1224, 1991, IF: 1,208/1992 (M21a).

Journal Articles (Q1 top 25% at WoS)

12. V. Jelisavcic, I. Stojkovic, V. Milutinovic, Z. Obradovic, "Fast learning of scale-free networks based on Cholesky factorization," *International Journal of Intelligent Systems*, Vol. 33(6), pp. 1322-1339, 2018, IF: 3,363 (M21).
13. Z. Babovic, J. Protic, and V. Milutinovic, "Web Performance Evaluation for Internet of Things Applications," *IEEE Access*, DOI: 10.1109/ACCESS.2016.2615181, Vol. 4, pp. 6974 – 6992, 2016, IF: 3,244 (M21).
14. S. Djordjevic, S. Stancin, A. Meglic, V. Milutinovic, S. Tomasic, "MC Sensor-A Novel Method for Measurement of Muscle Tension," *Sensors*, DOI: 10.3390/s111009411, Vol. 11 (10), pp. 9411-9425, 2011, IF: 1,739 (M21).

15. V. Milutinovic, "Our Profession Needs a Reminder," IEEE Computer, Vol. 39(5), pp. 102-104, 2006, IF: 1,289 (M21).
16. N. Milanovic, M. Malek, A. Davidson, V. Milutinovic, "Routing and Security in Mobile Ad Hoc Networks," IEEE Computer, Vol. 37(2), pp. 61-65, 2004, IF: 1,432 (M21).
17. V. Milutinovic, N. Skundric, "Will Distance Learning Create a Global University," IEEE Computer, Vol. 36(3), pp. 98-100, 2003, IF: 1,552 (M21).
18. E. Jovanov, V. Milutinovic, A. Hurson, "Acceleration of Nonnumeric Operations Using Hardware Support for the Ordered Table Hashing Algorithms," IEEE Transactions on Computers, Vol. 51(9), pp. 1026-1040, 2002, IF: 1,484 (M21).
19. A. Ngom, I. Stojmenovic, V. Milutinovic, "STRIP - a strip-based neural-network growth algorithm for learning multiple-valued functions," IEEE Transactions on Neural Networks, Vol. 12(2), pp. 212-227, 2001, IF: 1,479 (M21).
20. V. Milutinovic, P. Knezevic, B. Radunovic, S. Casselman, J. Schewel, "Obelix Searches Internet Using Customer Data," IEEE Computer, Vol. 33(7), pp. 104-107, 2000, IF: 1,043 (M21).
21. V. Milutinovic, D. Cvetkovic, J. Mirkovic, "Genetic Search Based on Multiple Mutations," IEEE Computer, Vol. 33(11), pp. 118-119, 2000, IF: 1,043 (M21).
22. V. Milutinovic, "Caching in distributed systems," IEEE Concurrency, Vol. 8(3), pp. 14-15, 2000, IF: 1,018 (M21).
23. M. Jovanovic, V. Milutinovic, "An overview of reflective memory systems," IEEE Concurrency, Vol. 7(2), pp. 56-64, 1999, IF: 0,784 (M21).
24. V. Milutinovic, M. Valero, "Enhancing and Exploiting the Locality," IEEE Transactions on Computers, Vol. 48(2), pp. 97-99, 1999, IF: 1,057 (M21).
25. I. Tartalja, V. Milutinovic, "Classifying Software-Based Cache Coherence Solutions," IEEE Software, Vol. 14(3), pp. 90-101, 1997, IF: 0,768 (M21).
26. D. Milutinovic, V. Milutinovic, "New solutions for new technologies," IEEE Computer, Vol. 29(4), 1996, IF: 0,608/1997 (M21).
27. A. Grujic, M. Tomasevic, V. Milutinovic, "A simulation study of hardware-oriented DSM approaches," IEEE Parallel & Distributed Technology Systems, Vol. 4(1), pp. 74-83, 1996, IF: 0,574/1997 (M21).
28. J. Protic, M. Tomasevic, V. Milutinovic, "Distributed shared memory: concepts and systems," IEEE Parallel & Distributed Technology Systems, Vol. 4(2), pp. 63-71, 1996, IF: 0,574/1997 (M21).
29. V. Milutinovic, Z. Petkovic, "Ten Lessons Learned from a RISC Design," IEEE Computer, Vol. 28(3), pp. 120, 1995, IF: 0,608/1997 (M21).
30. I. Ekmecic, I. Tartalja, V. Milutinovic, "EM³: A Taxonomy of Heterogeneous Computing Systems," IEEE Computer, Vol. 28(12), pp. 68-70, 1995, IF: 0,608/1997 (M21).
31. M. Tomasevic, V. Milutinovic, "Hardware approaches to cache coherence in shared-memory multiprocessors, Part 1," IEEE Micro, Vol. 14(5), pp. 52-59, 1994, IF: 0,992/1997 (M21).
32. M. Tomasevic, V. Milutinovic, "Hardware approaches to cache coherence in shared-memory multiprocessors 2.," IEEE Micro, Vol. 14(6), pp. 61-66, 1994, IF: 0,992/1997 (M21).
33. W. Helbig, V. Milutinovic, "A DCFL E/D-MESFET GaAs Experimental RISC Machine," IEEE Transactions on Computers, Vol. 38(2), pp. 263-274, 1989, IF: 1,388/1988 (M21).
34. V. Milutinovic, M. Bettinger, W. Helbig, "Multiplier/Shifter Design Tradeoffs in a 32-bit Microprocessor," IEEE Transactions on Computers, Vol. 38(6), pp. 874-881, 1989, IF: 1,388/1988 (M21).

35. V. Milutinovic, "A comparison of suboptimal detection algorithms applied to the additive mix of orthogonal sinusoidal signals," *IEEE Transactions on Communications*, Vol. 36(5), pp. 538-543, 1988, IF: 0,802 (M21).
36. V. Milutinovic, J. Crnkovic, C. Houstis, "A Simulation Study of Two Distributed Task Allocation Procedures," *IEEE Transactions on Software Engineering*, Vol. 14(1), pp. 54-61, 1988, IF: 1,387 (M21).
37. C. Gimarc, V. Milutinovic, "A Survey of RISC Processors and Computers of the Mid-1980s," *IEEE Computer*, Vol. 20(9), pp. 59-69, 1987, IF: 1,096 (M21).
38. V. Milutinovic, D. Fura, W. Helbig, J. Linn, "Architecture/Compiler Synergism in GaAs Computer Systems," *IEEE Computer*, Vol. 20(5), pp. 72-93, 1987, IF: 1,096 (M21).
39. D. Milutinovic, V. Milutinovic, B. Soucek, "The honeycomb architecture," *IEEE Computer*, Vol. 20 (4), pp. 81-83, 1987, IF: 1,096 (M21).
40. B. Furht, V. Milutinovic, "A Survey of Microprocessor Architectures for Memory Management," *IEEE Computer*, Vol. 20(3), pp. 48-67, 1987, IF: 1,096 (M21).
41. V. Milutinovic, N.é Lopez-Benitez, K. Hwang, "A GaAs-Based Microprocessor Architecture for Real-Time Applications," *IEEE Transactions on Computers*, Vol. 36(6), pp. 714-727, 1987, IF: 1,517 (M21).
42. V. Milutinovic, "A Simulation Study of the Vertical-Migration Microprocessor Architecture," *IEEE Transactions on Software Engineering*, Vol. 13(12), pp. 1265-1277 1987, IF: 1,402 (M21).
43. V. Milutinovic, "Guest Editor's Introduction GaAs Microprocessor Technology," *IEEE Computer*, Vol. 19(10), pp. 10-13, 1986, IF: 1,111 (M21).
44. V. Milutinovic et al. "Issues of Importance in Designing GaAs Microcomputer Systems," *IEEE Computer*, Vol. 19(10), pp. 45-57, 1986, IF: 1,111 (M21).
45. A. Silbey, V. Milutinovic, V. Mendoza-Grado, "A Survey of Advanced Microprocessors and HLL Computer Architectures," *IEEE Computer*, Vol. 19(8), pp. 72-85, 1986, IF: 1,111 (M21).
46. V. Milutinovic, D. Fura, W. Helbig, "An Introduction to GaAs Microprocessor Architecture for VLSI," *IEEE Computer*, Vol. 19(3), pp. 30-42, 1986, IF: 1,111 (M21).
47. Veljko Milutinović, "A microprocessor-oriented algorithm for adaptive equalization," *IEEE Transactions on Communications*, Vol. 33(6), pp. 522-526, 1985, IF: 0,983 (M21).
48. V. Milutinovic, "Comparison of three suboptimum detection procedures," *Electronics Letters*, Vol. 16(17), pp. 681-683, 1980, IF: 1,345/1981 (M21).
49. V. Milutinovic, "Suboptimum detection procedure based on the weighting of partial decisions," *Electronics Letters*, Vol. 16(6), pp. 237-238, 1980, IF: 1,345/1981 (M21).

Journal Articles (Q2 top 50% at WoS)

50. N. Korolija, D. Bojic, AR Hurson, V. Milutinovic, "A runtime job scheduling algorithm for cluster architectures with dataflow accelerators," *Advances in Computers*, vol 126(1), pp. 201-245, 2022.
51. M. Kotlar, M. Punt, V. Milutinovic, "Energy efficient implementation of tensor operations using dataflow paradigm for machine learning," *Advances in Computers*, vol 126(1), pp. 151-199, 2022.
52. V. Milutinovic et al., "VLSI for SuperComputing: Creativity in R+D from applications and algorithms to masks nad chips," *Advances in Computers*, vol 126(1), pp. 1-14, 2022.
53. M. Kotlar, M. Punt, Z. Radivojevic, M. Cvetanovic, V. Milutinovic, "Novel meta-features for automated machine learning model selection in anomaly detection," *IEEE Access*, vol 9, 2021.

54. M. Banković, V. Filipović, J. Graovac, J. Hadži-Purić, A. R Hurson, A. Kartelj, J. Kovačević, N. Korolija, M. Kotlar, N. B Krdžavac, F. Marić, S. Malkov, V. Milutinović, N. Mitić, S. Mišković, M. Nikolić, G. Pavlović-Lažetić, D. Simić, S. Stojanović Djurdjević, S. Vujičić Stanković, M. Vujošević Janičić, M. Živković, "Teaching graduate students how to review research articles and respond to reviewer comments," *Advances in Computers*, vol 116(1), pp. 1-63, 2020.
55. M. Banković, Z. Djordjević, V. Filipović, J. Graovac, J. Hadži-Purić, A. Hurson, A. Kartelj, J. Kovačević, N. Korolija, M. Kotlar, I. Kuraj, N. Krdžavac, F. Marić, S. Malkov, V. Milutinović, N. Mitić, S. Mišković, M. Nikolić, G. Pavlović-Lažetić, D. Simić, S. Stojanović-Djurdjević, S. Vujičić-Stanković, M. Vujošević-Janičić, M. Živković, "Teaching Graduate Students How to Review Research Articles and How to Respond to Reviewer Comments," *Advances in Computers*, Elsevier, vol. 124, 2018, IF: 1,514 (M22).
56. A. Hurson, V. Milutinovic, "Preface," *Advances in Computers*, vol. 106, pp. IX-X, 2017, IF: 1,514 (M22).
57. V. Milutinovic, S. Vujicic-Stankovic, A. Jovic, D. Draskovic, M. Misic, D. Furundzic, "A New Course on R&D Project Management in Computer Science and Engineering: Subjects Taught, Rationales Behind, and Lessons Learned," *Advances in Computers*, vol. 105, pp. 1- 19, 2017, IF: 1,514 (M22).
58. A. Hurson, V. Milutinovic, "Creativity in Computing and Dataflow Supercomputing - Preface," *Advances in Computers*, vol. 104, pp. VII-VIII, 2017, IF: 1,514 (M22).
59. Z. Stanisavljevic, B. Nikolic, I.Tartalja, V. Milutinovic, "A classification of eLearning tools based on the applied multimedia", *Multimedia Tools and Applications*, vol. 74, Issue 11, pp 3843-3880, June 2015, IF: 1,221 (M22).
60. B. Furlan, B. Nikolic, V. Milutinovic, "A Survey and Evaluation of State-of-the-Art Intelligent Question Routing Systems", *International Journal Of Intelligent Systems*, vol. 28, Issue 7, pp. 686–708, 2013, IF: 1.411 (M22).
61. S. Tomazic, V. Pavlovic, J. Milovanovic, J. Sodnik, A. Kos, S. Stancin, V. Milutinovic, "Fast file existence checking in archiving systems," *ACM Transactions on Storage*, vol. 7(1), 2011, IF:1,115/2012 (M22).
62. Z. Tafa, G. Rakocevic, D. Mihailovic, V. Milutinovic, "Effects of Interdisciplinary Education on Technology-Driven Application Design, " *IEEE Transactions on Education*, vol. 54(3), pp. 462-470, 2011, IF: 1,021 (M22).
63. B. Nikolic, Z. Radivojevic, J. Djordjevic, V. Milutinovic, "A Survey and Evaluation of Simulators Suitable for Teaching Courses in Computer Architecture and Organization," *IEEE Transaction on Education*, Vol. 52(4), pp. 449-458, 2009, IF: 0,822 (M22).
64. A. Milenkovic, V. Milutinovic, "A quantitative analysis of wiring lengths in 2D and 3D VLSI," *Microelectronics journal*, Vol. 29 (6), pp. 313-321, 1998, IF: 0,345 (M22).
65. P. Chow, T. Geigel, V. Milutinovic, J. Pridmore, "Impact of mapping parameters on the performance of small cache memories," *Microprocessors and Microsystems - Embedded Hardware Design*, Vol. 12(4), pp. 197-205, 1988, IF: 0,404 (M22).
66. A. Kabakibo, V. Milutinovic, "Simulation study of the impact of technology on cache memory performance," *Microprocessors and Microsystems - Embedded Hardware Design*, Vol. 12(5), pp. 277-285, 1988, IF: 0,404 (M22).
67. H. Vlahos, V. Milutinovic, "GaAs microprocessors and digital systems: an overview of R&D efforts," *IEEE Micro*, Vol. 8(1), pp. 28-56, 1988, IF: 0,595 (M22).
68. V. Milutinovic, J. A. B. Fortes, L. Jamieson, "A multimicroprocessor architecture for real-time computation of a class of DFT algorithms," *IEEE Transactions on Acoustics, Speech, and Signal Processing*, Vol. 34(5), pp. 1301-1309, 1986, IF: 1,032 (M22).

Journal Articles (Q3 and Q4 - all 100% at WoS)

69. M Kotlar, D Bojić, M Punt, V Milutinović, "Survey of deployment locations and underlying hardware architectures for contemporary deep neural networks," *International Journal of Distributed Sensor Networks* 15 (8), 2019.
70. M. Cvetanovic, Z. Radivojevic, V. Milutinovic, "Restart Optimization for Transactional Memory with Lazy Conflict Detection," *International Journal of Parallel Programming*, Vol. 45(3), pp. 482-507, 2017, IF: 0.897, (M23).
71. V. Blagojević, D. Bojić, M. Bojović, M. Cvetanović, J. Đorđević, Đ. Đurđević, B. Furlan, S. Gajin, Z. Jovanović, D. Milićev, V. Milutinović, B. Nikolić, J. Protić, M. Punt, Z. Radivojević, Ž. Stanisavljević, S. Stojanović, I. Tartalja, M. Tomašević, P. Vuletić, "A Systematic Approach to Generation of New Ideas for PhD Research in Computing," *Advances in Computers*, vol. 104, 2016, IF: 0,303 (M23).
72. V. Milutinovic, B. Furht, Z. Obradovic, N. Korolija, "Advances in High Performance Computing and Related Issues - Editorial," *Mathematical Problems in Engineering*, 2016, IF: 0,802 (M23).
73. I. Ratkovic, N. Bezanic, O. Ünsal, A. Cristal, V. Milutinovic, "Chapter One - An Overview of Architecture-Level Power- and Energy-Efficient Design Techniques," *Advances in Computers*, vol. 98, pp. 1-57, 2015, IF: 0,302/2014 (M23).
74. A. Hurson, V. Milutinovic, "Preface," *Advances in Computers*, vol. 96, pp. vii-viii, 2015, IF: 0,256/2015 (M23).
75. Z. Sustran, G. Rakocevic, V. Milutinovic, "Dual Data Cache Systems: Architecture and Analysis", *Advances in Computers*, vol 96, pp. 187-233, 2015, IF: 0,256/2015 (M23).
76. A. Kos, S. Tomazic, J. Salom, N. Trifunovic, M. Valero, V. Milutinovic, "New Benchmarking Methodology and Programming Model for Big Data Processing," *International Journal of Distributed Sensor Networks*, vol. 2015, 2015, IF: 0,665/2014 (M23).
77. G. Rakocevic, Z. Tafa, V. Milutinovic, "A Novel Approach to Data Mining in Wireless Sensor Networks," *Ad Hoc & Sensor Wireless Networks*, Vol. 22 (1-2), pp. 21-40, 2014, IF: 0,435 (M23).
78. M. Radulovic, M. Tomasevic, V. Milutinovic, "Register-Level Communication in Speculative Chip Multiprocessors", *Advances in Computers*, vol, 92, pp. 1-66, 2014, IF: 0,302 (M23).
79. A. Vitorovic, M. Tomasevic, V. Milutinovic, "Manual Parallelization Versus State-of-the-Art Parallelization Techniques: The SPEC CPU2006 as a Case Study," *Advances in Computers*, vol. 92, pp. 203-251, 2014, IF: 0,302 (M23).
80. Z. Babovic, V. Milutinovic, "Novel System Architectures for Semantic-Based Integration of Sensor Networks," *Advances in Computers*, vol. 90, pp. 91-183, 2013, IF:0,489 (M23).
81. S. V. Stankovic, N. Kojic, G. Rakocevic, D. Vitas, V. Milutinovic, "A Classification of Data Mining Algorithms for Wireless Sensor Networks, and Classification Extension to Concept Modeling in System of Wireless Sensor Networks Based on Natural Language Processing," *Advances in Computers*, vol. 90, pp. 223-283, 2013, IF:0,489 (M23).
82. Z. Jovanovic, V. Milutinovic, "FPGA accelerator for floating-point matrix multiplication, " *IET Computers & Digital Techniques*, vol. 6(4), pp. 249-256, 2012, IF: 0,284 (M23).
83. S. Omerovic, Z. Babovic, Z. Tafa, V. Milutinovic, S. Tomazic, "Concept modeling: From origins to multimedia," *Multimedia Tools and Applications*, vol. 51(3), pp. 1175-1200, 2011, IF:0,617 (M23).
84. Z. Tafa, V. Milutinovic, "Detectability of Static and Moving Targets in Randomly Deployed Military Surveillance Networks," *Ad Hoc & Sensor Wireless Networks*, Vol. 13(3-4), pp. 291-312, 2011, IF: 0,400 (M23).
85. M. Jovic, G. Rakocevic, M. Jovic, V. Milutinovic, "A Multi-, Inter-, and Trans-Disciplinary Approach to Teaching Wireless Sensor Networks," *Technics Technologies Education Management*, vol. 6(4), pp. 977-984, 2011, IF:x.

86. S. Omerovic, S. Tomazic, M. Milutinovic, V. Milutinovic, "Methodology for Written and Oral Presentation of Research Results," *Journal of Professional Issues In Engineering Education and Practice*, Vol 136 (2), pp. 112-117, 2010, IF: 0,372 (M23).
87. J. Sahuquillo, S. Petit, A. Pont, V. Milutinovic, "Exploring the performance of split data cache schemes on superscalar processors and symmetric multiprocessors," *Journal of Systems Architecture*, Vol. 51(8), pp. 451-469, 2005, IF:0,402 (M23).
88. V. Milutinovic et al. "Testing the E-Business Infrastructure: Expanding into the Wireless/Mobile Environments," *Telecommunication Systems*, Vol. 22(1-4), pp. 141-150, 2003, IF: 0,413 (M23)
89. M. Kovacevic, M. Diligenti, M. Gori, V. Milutinovic, "Recognition of Common Areas in a Web Page Using a Visualization Approach," in *Artificial Intelligence: Methodology, Systems, and Applications*, Lecture Notes in Computer Science, Vol. 2443, pp. 203-212, 2002, IF: 0,515 (M23).
90. A. Milenkovic, V. Milutinovic, "A performance evaluation of cache injection in bus-based shared memory multiprocessors," *Microprocessors and Microsystems*, Vol. 26(2), pp. 51-61, 2002, IF: 0,341 (M23).
91. D. Horvat, D. Cvetkovic, V. Milutinovic, P. Kocovic, V. Kovacevic, "Mobile Agents and Java Mobile Agents Toolkits," *Telecommunication Systems*, Vol. 18(1-3), pp. 271-287, 2001, IF: 0,109 (M23).
92. D. Cvetkovic, M. Pesic, D. Petkovic, V. Milutinovic, D. Horvat, P. Kocovic, V. Kovacevic, "Architecture of the Mobile Environment for Intelligent Genetic Search and Proxy Caching," *Telecommunication Systems*, Vol. 18(1-3), pp. 255-270, 2001, IF: 0,109 (M23).
93. J. Sahuquillo, A. Pont, V. Milutinovic, "The Filter Data Cache: A Tour Management Comparison with Related Split Data Cache Schemes Sensitive to Data Localities," *Lecture Notes in Computer Science*, Vol. 1940, pp. 319-327, 2001, IF: 0,415 (M23).
94. G. Davidovic, J. Ciric, V. Milutinovic, G. Hadzic, L. Radicevic, "VLSI implementation of detection of R2 signalization," *Microprocessors and Microsystems, Embedded Hardware Design*, Vol. 21(2), pp. 73-78, 1997, IF: 0,163 (M23).
95. M. Tomasevic, V. Milutinovic, "The word-invalidate cache coherence protocol," *Microprocessors and Microsystems - Embedded Hardware Design*, Vol. 20(1), pp. 3-16, 1996, IF: 0,163/1997 (M23).
96. S. Lakhani, Y. Wang, A. Milenković, V. Milutinović, "2D matrix multiplication on a 3D systolic array," *Microelectronics journal*, Vol. 27(1), pp. 11-22, 1996, IF: 0,227/1997 (M23).
97. S. Savic, M. Tomasevic, V. Milutinovic, A. Gupta, M. Natale, I. Gertner, "Improved RMS for the PC environment," *Microprocessors and Microsystems - Embedded Hardware Design*, Vol. 19(10), pp. 609-619, 1995, IF: 0,163/1997 (M23).
98. V. Milutinovic, "Microprocessor architecture and design for gallium arsenide technology (part 2)," *Microelectronics Journal*, Vol. 20(3), pp. 7-9, 1989. (M23).
99. 61. V. Milutinovic, "Microprocessor architecture and design for GaAs technology," *Microelectronics Journal*, Vol. 19(4), pp. 51-55, 1988, (M23).
100. K. McNeley, V. Milutinovic, "Emulating a Complex Instruction Set Computer with a Reduced Instruction Set Computer," *IEEE Micro*, Vol. 7(1), pp. 60-72, 1987, IF: 0,392 (M23).
101. V. Milutinovic, "Performance comparison of two suboptimum detection procedures in real environment," *IEE Proceedings F. Communications, Radar and Signal Processing*, Vol. 131(4), pp. 341-344, 1984, (M23).

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